

Faculty of Engineering, Department of Electrical and Computer Engineering

University of Victoria

Limited-Term Assistant Teaching Professor

The Department of Electrical and Computer Engineering, University of Victoria, invites applications from talented scholars for a limited-term position at the rank of Assistant Teaching Professor in the area of Electrical and Computer Engineering. The successful candidate will have a one-year appointment. There may be a possibility of renewal for a second year. The anticipated start date is May 1, 2020.

The University of Victoria is consistently ranked in the top tier of Canada's research-intensive universities. Vital impact drives the UVic sense of purpose. As an internationally renowned research and teaching hub, we tackle essential issues that matter to people, places and the planet. Our commitment to research-inspired dynamic learning and vital impact make this Canada's most extraordinary environment for discovery and innovation.

The University of Victoria Campus is located on the traditional lands of the Coast Salish Peoples and we are privileged to do our work in a way that is inspired by their history, customs, and culture. Please see the University of Victoria's [Territory Acknowledgement](#) for more information.

The Department of Electrical and Computer Engineering in the Faculty of Engineering is committed to the highest standards of education and research so that together we can create a sustainable and healthy future with intelligent systems that improve the quality of life for everyone. The department has 30 faculty members with internationally recognized research and teaching programs. Six of our faculty members are IEEE Fellows, three are Fellows of the Canadian Academy of Engineering, and four are fellows of the Engineering Institute of Canada.

Qualified candidates will have:

- Masters and/or Ph.D. in Electrical and Computer Engineering, or a related discipline at the time of appointment.
- Ability to teach undergraduate courses in communications systems, network protocols, wireless and mobile systems, and stochastic processes. The successful candidate should also have the ability to teach undergraduate courses in at least one of the following areas: electronics, digital system design, and/or embedded systems.
- Demonstration of outstanding teaching abilities evidenced by data, testimonials, and other forms of documentation.
- Demonstrated ability to teach to a diverse student body with a wide range of perspectives, experience, and cultures.
- Ability to give effective feedback to diverse students.

- Demonstrated ability to work collaboratively in an academic unit.
- Professional Engineering (P.Eng.) registration in the province of British Columbia (or eligibility to register) is a necessary condition for the appointment. Please clearly indicate your status or eligibility.

The Faculty of Engineering is seeking to hire an exceptional faculty member with a preference for women candidates. This is in accordance with the University of Victoria's Equity Plan, and pursuant to Section 42 of the BC human rights code. The Faculty and Department are committed to an environment that nurtures high quality learning for all students and values equity, diversity, and inclusivity. The University encourages applicants of all backgrounds to apply, particularly Indigenous peoples, women, persons with disabilities, and members of visible minorities. Candidates who wish to qualify for preferential consideration, must self-identify in their application materials in the cover letter.

To be considered, please submit a completed application package including a cover letter that addresses the full scope of the job requirements, along with your curriculum vitae, evidence of teaching skills (teaching statements, syllabi, evaluation data), statement of diversity knowledge, experience and skills, and contact information for 3 references at <https://academicjobsonline.org/ajo/jobs/15993>, addressed to the chair of the Department of Electrical and Computer Engineering. In order to be considered, application packages must be received by March 24, 2020. You are asked to upload your C.V. and other personal information to this service, which is provided for the convenience of you and your referees. The service stores data on servers outside of Canada; the data is therefore not in the custody or under the control of the University of Victoria. You may wish to review the privacy statement at <https://academicjobsonline.org>. If you do not wish to use this service, please submit your complete application package to eeasst@uvic.ca.

Please note that reference and background checks, including credential and degree verification, may be undertaken as part of this recruitment process.

- Faculty and Librarians at the University of Victoria are governed by the provisions of the Collective Agreement. Members are represented by the University of Victoria Faculty Association (<http://www.uvicfa.ca>).
- UVic is committed to upholding the values of equity, diversity, and inclusion in our living, learning and work environments. In pursuit of our values, we seek members who will work respectfully and constructively with differences and across levels of power. We actively encourage applications from members of groups experiencing barriers to equity. Read our full equity statement here: www.uvic.ca/equitystatement.
- The University of Victoria Campus is located on the traditional lands of the Coast Salish Peoples and we are privileged to do our work in a way that is inspired by their history, customs, and culture.
- Persons who anticipate needing accommodations for any part of the application and hiring process may contact the Faculty Relations and Academic Administration in the Office of the VP Academic and Provost at FRrecruit@uvic.ca. Any personal information provided will be maintained in confidence.

- The University of Victoria acknowledges the potential impact that career interruptions can have on a candidate's record of research achievement and encourages applicants to explain in their application the impact that career interruptions may have had on their record of research achievement.
- All qualified candidates are encouraged to apply; in accordance with Canadian immigration requirements, Canadians and permanent residents will be given priority. Please indicate in your application package if you are a Canadian citizen or permanent resident.

If you have any questions, please do not hesitate to contact us:

Michael McGuire, Chair
Department of Electrical and Computer Engineering
University of Victoria
P.O. Box 1700 STN CSC
Victoria, B.C. V8W 2Y2
Canada

Telephone: +1-250-721-8684

E-mail: <mailto:ecechair@uvic.ca>

Web: <https://www.uvic.ca/engineering/ece/index.php>